

## ABSTRACT

Disclosed here is a method of aging a plasma display panel. The aging method of the present invention contains a first aging period and a second aging period. In the first aging period, applying voltage  $V_{d1}$  to at least any  
5 one of the scan electrodes, the sustain electrodes, and the address electrodes suppress self-erase discharge that occurs in the wake of aging voltage generated by application of voltage in which the scan electrodes take a voltage level higher than the sustain electrodes. In the second aging period, applying voltage  $V_{d2}$  to at least any one of the scan electrodes, the sustain electrodes, and the  
10 address electrodes suppress self-erase discharge that occurs in the wake of aging voltage generated by application of voltage in which the sustain electrodes take a voltage level higher than the scan electrodes.

The above aging method offers a power-efficient aging process with the aging time accelerated.